

J. Stevenson
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Akihiko HARADA et al.

Serial Number: Not Yet Assigned

Filed: November 22, 2000

For: **INSULATED GATE TYPE SEMICONDUCTOR DEVICE AND METHOD
FOR FABRICATING SAME**



INFORMATION DISCLOSURE STATEMENT

Director of Patents and Trademarks
Washington, D.C. 20231

November 22, 2000

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge

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Respectfully submitted,
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A handwritten signature in black ink, appearing to read "Stephen G. Adrian".

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